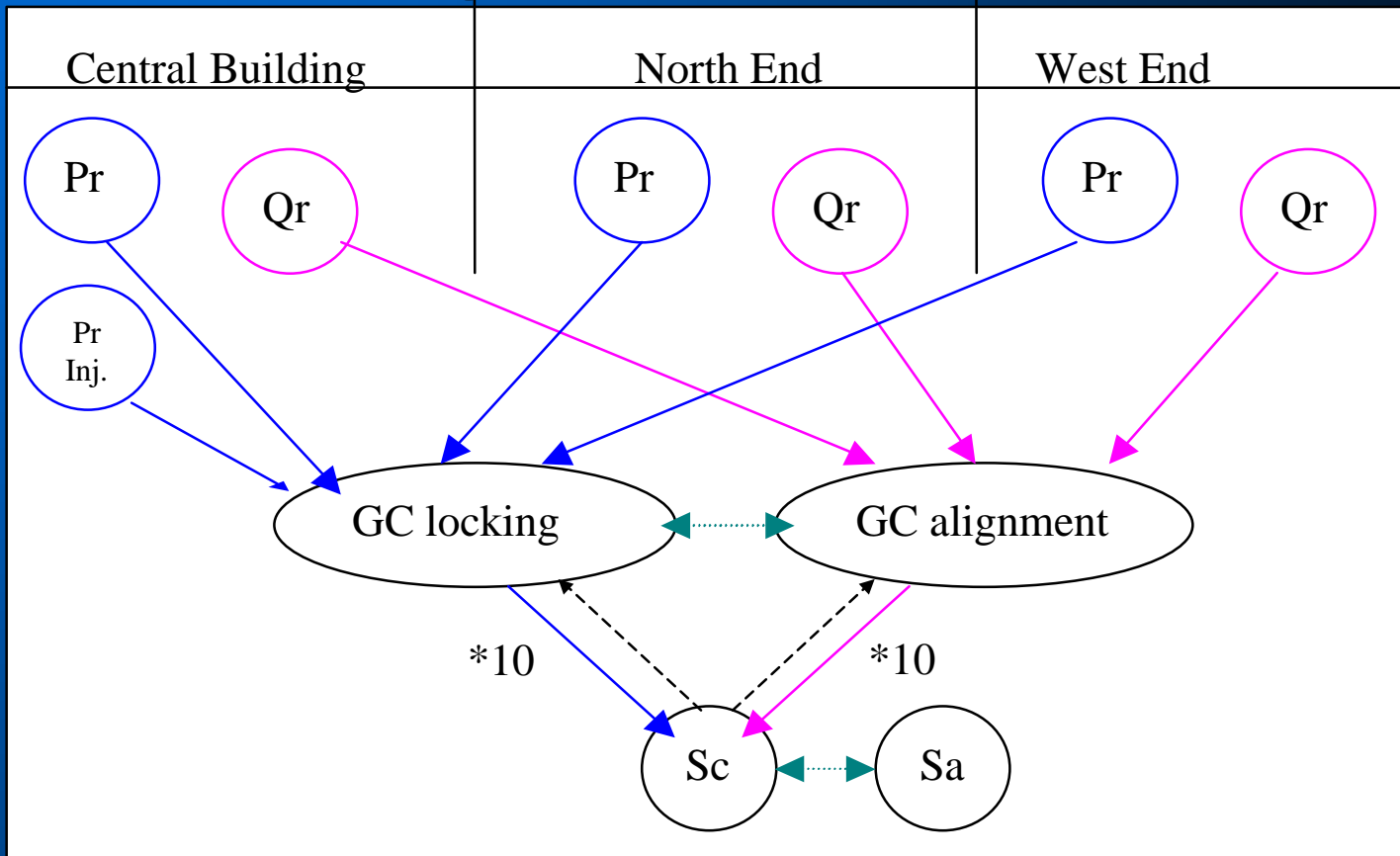
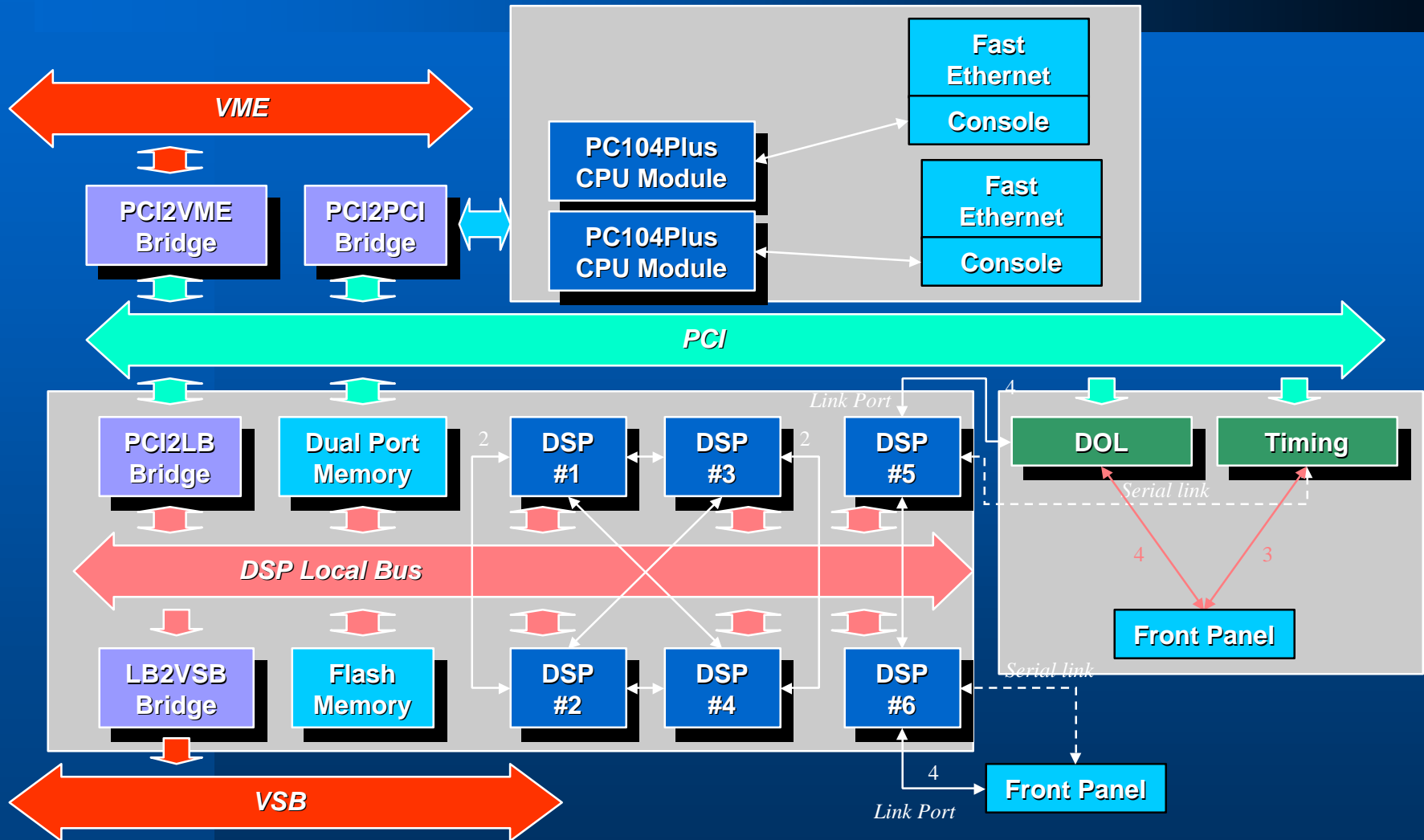


Connections

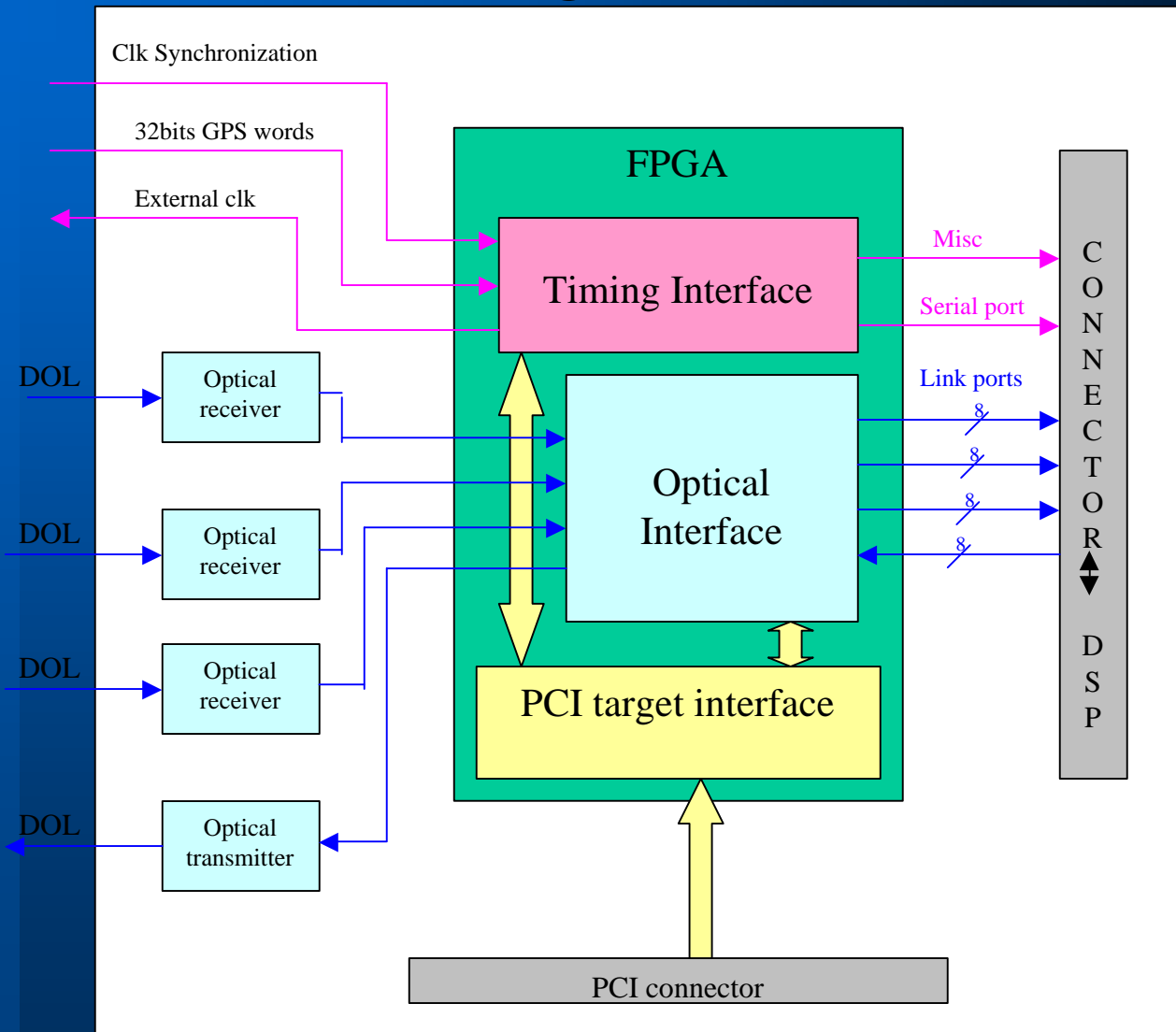


	Timing System	DOL input	DOL output	Local Input	Local output
Photodiode Readout	1	0	1	0	0
Global Control	1	4 (+10?)	10	1	1
Suspension	1	2	0 (1?)	1	1

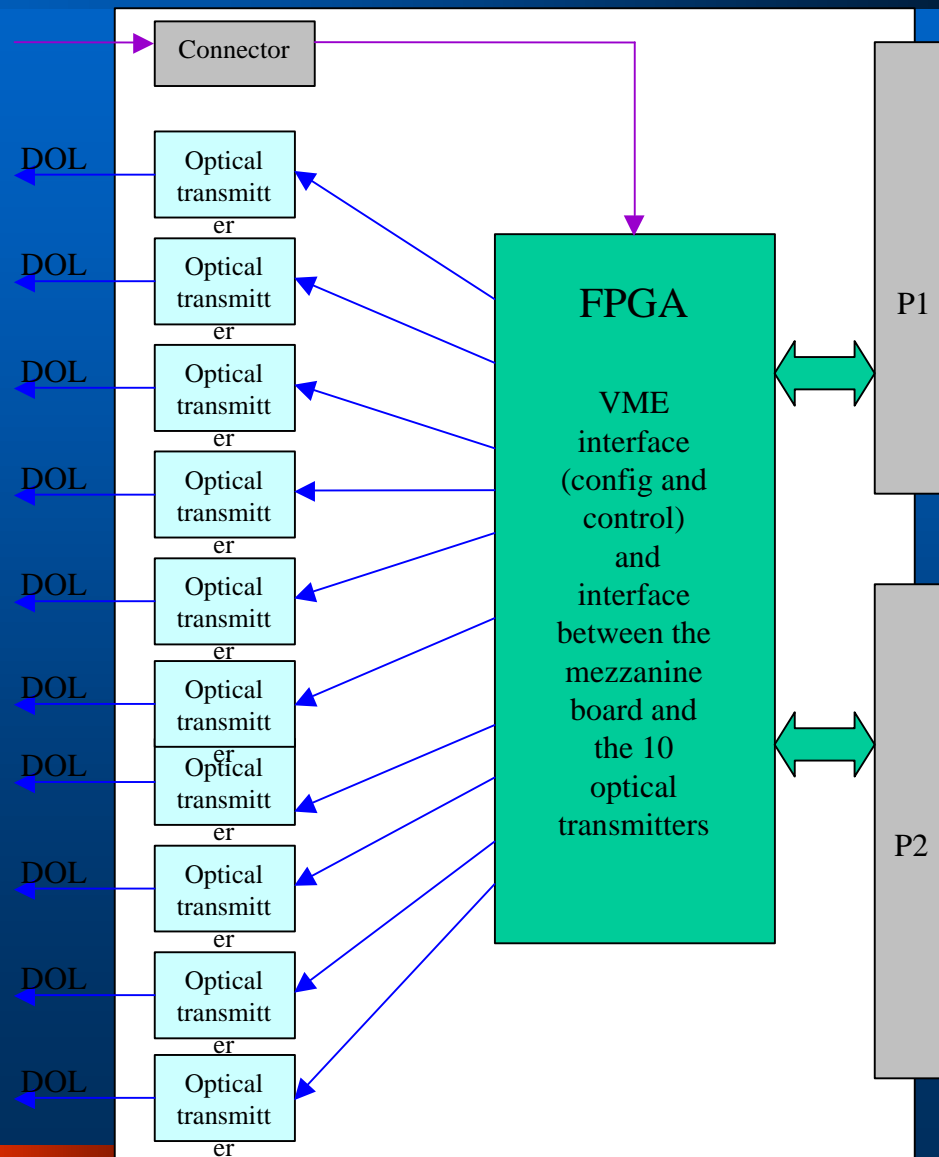
Block Diagram



DOL/Timing mezzanine



DOL Extension (Global Control)



DSP
7 x 14

The diagram shows a VME bus board layout. A light blue rectangle represents the board, with a total width of 14.8 cm and a height of 4 cm. On the left side, a cyan rectangle represents the DSP, measuring 7 x 14. In the center, a dark blue rectangle represents the PC104Plus, measuring 9.5 x 9. On the right side, a light blue rectangle represents the DOL & Timing, measuring 5.5 cm in width. At the bottom, a red rectangle represents the VME Connectors. The DSP and PC104Plus are positioned such that their top edges are aligned with the top edge of the board. The DOL & Timing component is positioned to the right of the PC104Plus, with its top edge also aligned with the top edge of the board. The VME Connectors are located at the bottom edge of the board.

14.8 cm

4 cm

PC104Plus
9.5 x 9

DOL
&
Timing

5.5 cm

VME Connectors