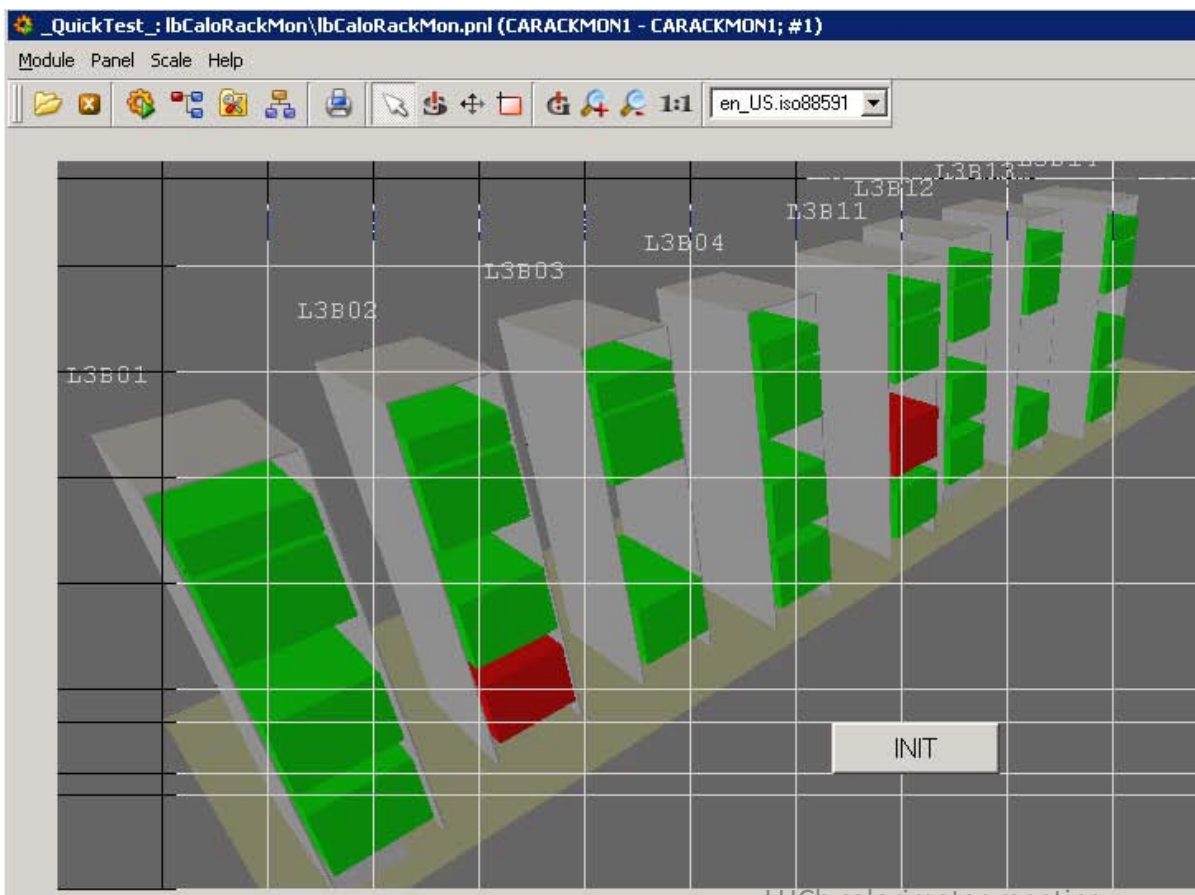
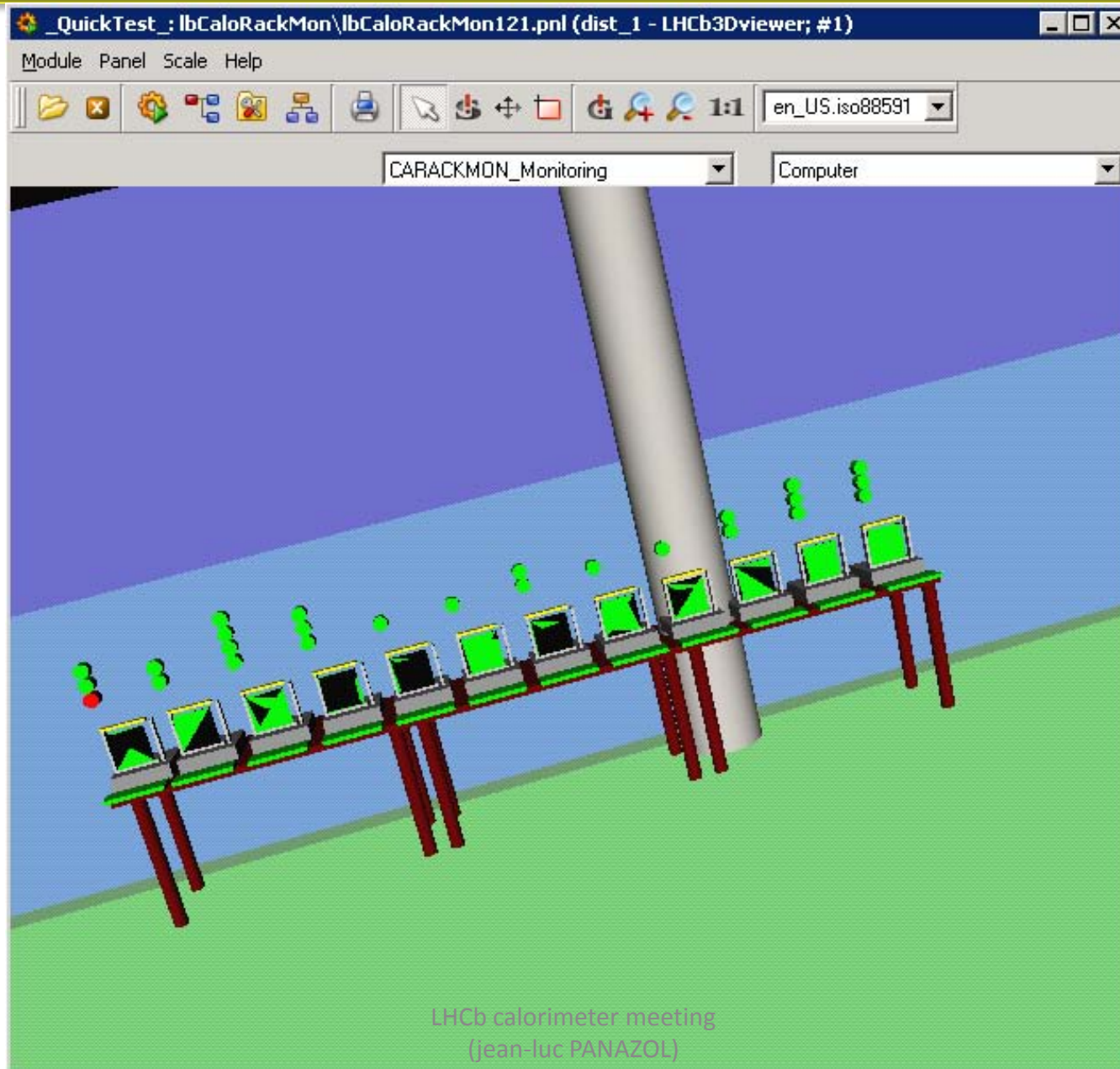


# Panel 3D = XML file pointer

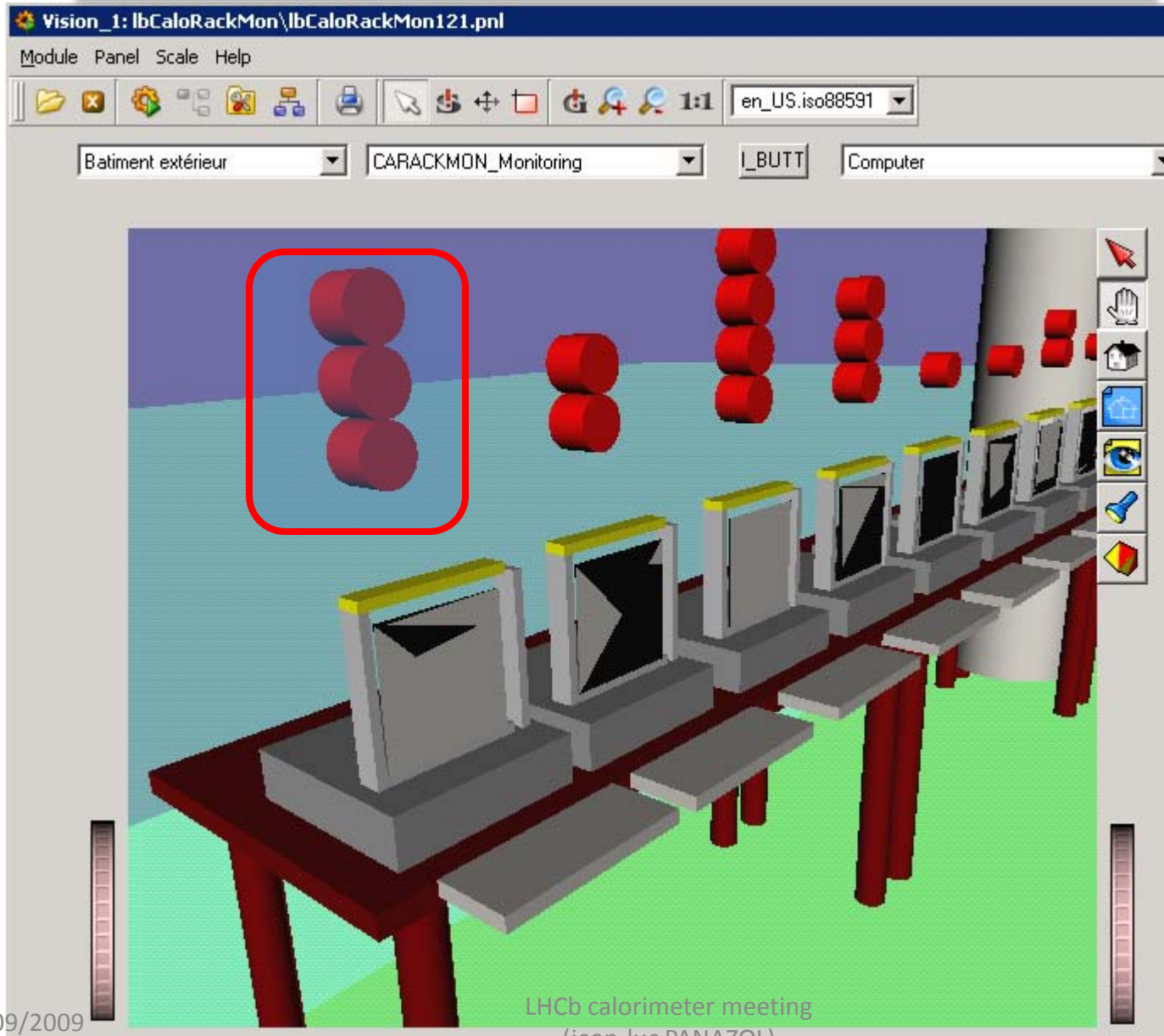


```
G:\calo\pvss\fwComponents_CARACKMON...
G:\calo\pvss\fwComponents_CARACKMON...
File Edit View Favorites Tools Help
G:\calo\pvss\fwComponen...
- <box_L3B01>
  <name>L3B01</name>
  <x>0</x>
  <y>0</y>
  <z>-540</z>
  <U1>10</U1>
  <dy>460</dy>
  <dx>110</dx>
  <dz>180</dz>
  <fill>0</fill>
- <text_L3B01>
  <name>text_L3B01</name>
  <y>500</y>
  <z>-200</z>
  <textStr>L3B01</textStr>
  <fontsize>14</fontsize>
  <fill>1</fill>
</text_L3B01>
+ <box_L3B01_2>
+ <box_L3B01_3>
+ <box_L3B01_5>
- <box_L3B01_7>
  <name>ECAL13</name>
  <y>40</y>
  <x>5</x>
  <z>10</z>
  <dx>100</dx>
  <dy>85</dy>
  <fill>1</fill>
</box_L3B01_7>
</box_L3B01>
```

# An another XML file pointer



# Example : PVSS project monitoring



.\IbCaloRackMon\IbCaloComputerMon.pnl (dist\_1 - LHCb3Dviewer; #1)

dist\_1: ECqhva01wPRSDAQA1

PROCESS RUNNING

```
PVSS00pmon  
PVSS00data  
PVSS00event  
PVSS00ctrl => -f pvss_scripts.lst  
PVSS00sim  
PVSS00dist  
PVSS00dim => -dim_dp_config SpecsConfig  
PVSS00ctrl => fwSpecsScript.ctf  
PVSS00ctrl => unDistributedControl.ctf  
PVSS00ctrl => fwFsmSrvr  
PVSS00dim => -num 2 -dim_dp_config SpecsConfig_ControlBoard  
PVSS00dim => -num 3 -dim_dp_config SpecsConfig_CRDC  
PVSS00dim => -num 4 -dim_dp_config SpecsConfig_LEDTSB  
PVSS00dim => -num 5 -dim_dp_config SpecsConfig_PSFEB
```

PROCESS STOP

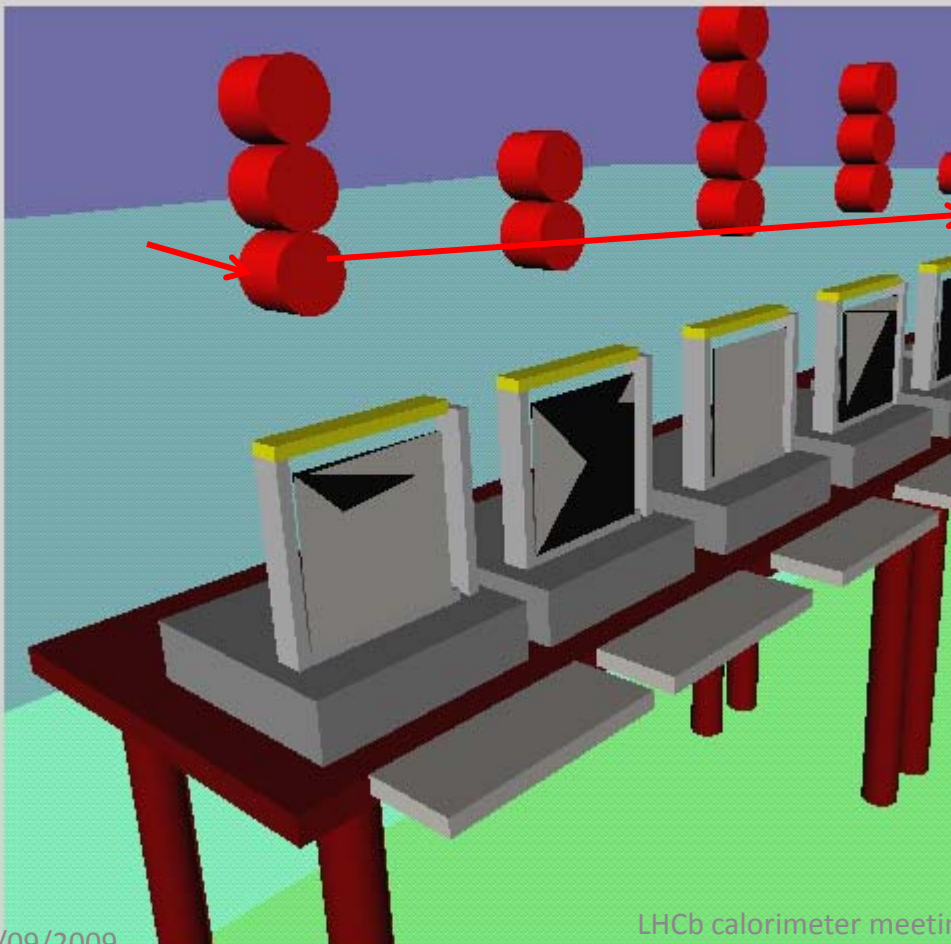
```
PVSS00valarch => -num 0  
PVSS00valarch => -num 1  
PVSS00valarch => -num 2  
PVSS00valarch => -num 3  
PVSS00valarch => -num 4  
PVSS00valarch => -num 5  
PVSS00ui => -m gedi  
PVSS00ui => -p fwInstallation/fwInstallationAgent.pnl -iconBar -menuBar  
PVSS00ctrl => -f fwScripts.lst  
PVSS00ui => -p fwHw/Hw.pnl -iconBar -menuBar  
PVSS00ui => -p fwSpecs/SpecsClient.pnl -iconBar -menuBar  
PVSS00ui => -p fwDeviceEditorNavigator/fwDeviceEditorNavigator.pnl -iconBar  
PVSS00ui => -p fwDIM/fwDim.pnl -menuBar -iconBar
```

PROCESS STARTING

PROCESS IN ERROR

Batiment extérieur





Status Page [with Auto Refresh long](#) / [without Refresh long](#)

## Project: ECDAQ1

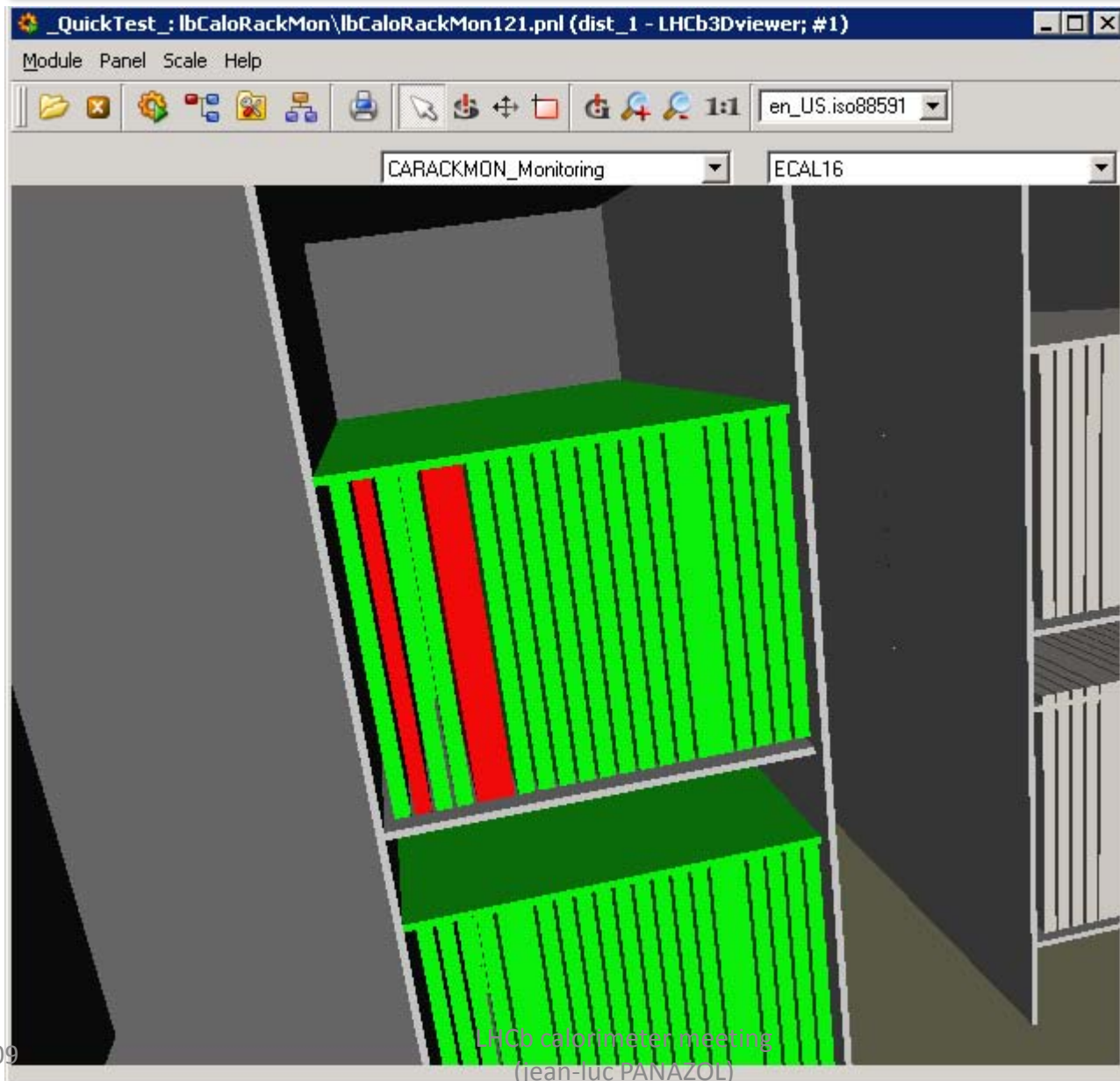
Project:

Manager:

Manager Status of 39 entries @ 2009.08.28 15:49:05 W. Europe Daylight Time

Select	State	Manager	PID	Starttime	Opti
<input type="radio"/>	running	PVSS00pmon (1)	5100	2009.08.11 15:27:43.371	
<input type="radio"/>	running	PVSS00data (0)	4604	2009.08.11 15:27:43.965	
<input type="radio"/>	stopped	PVSS00valarch (0)	-1	1970.01.01 01:00:00.000	-num 0
<input type="radio"/>	stopped	PVSS00valarch (1)	-1	1970.01.01 01:00:00.000	-num 1
<input type="radio"/>	stopped	PVSS00valarch (2)	-1	1970.01.01 01:00:00.000	-num 2
<input type="radio"/>	stopped	PVSS00valarch (3)	-1	1970.01.01 01:00:00.000	-num 3
<input type="radio"/>	stopped	PVSS00valarch (4)	-1	1970.01.01 01:00:00.000	-num 4
<input type="radio"/>	stopped	PVSS00valarch (5)	-1	1970.01.01 01:00:00.000	-num 5
<input type="radio"/>	running	PVSS00event (0)	4044	2009.08.11 15:29:12.167	
<input type="radio"/>	running	PVSS00ctrl(1)	5020	2009.08.11 15:29:37.119	-f pvss_scripts.lst
<input type="radio"/>	running	PVSS00sim(1)	6028	2009.08.11 15:29:37.869	

# Example : Electronique board monitoring



# Example : Electronique board CROC

The screenshot displays the 'Vision\_1: CROC' control software interface. The window title is 'Vision\_1: CROC'. The main interface is divided into several functional areas:

- Control / Monitoring:** Includes 'Slot Power' controls with state and Ctrl indicators for 16 slots, 'All OFF', 'All ON', and 'Clear' buttons. Below this is a 'Crate ID' field (set to 0) and a 'Temp(\*C)' field.
- Warnings:** A panel on the right listing warnings such as Power, Temp, FEB Synchr, L0 TTC Hdr, TTC Ready, Locked, and Parity, each with a status indicator.
- Reset:** A panel with buttons for 'CROC', 'Clk Redir.', 'Delay Chip', 'TTCrq', 'FePGA', and 'GOL'.
- DAQ Synchronisation:** A panel with input fields for FE 0, FE 1, FE 2, FE 3, and Sync, each with an 'Apply' button.
- FE Synchronisation:** A panel with 'FEB' and 'CROC' fields and 'Apply' buttons.
- Clock Settings:** A panel with 'Fine' and 'Coarse' fields and an 'Apply' button.
- General Control:** A panel with 'Mode Clock' and 'Mode L0/Ch B' fields and an 'Apply' button.
- Optical Mezzanine Mode:** A panel at the bottom with state indicators for Physics and BER, and 'Left' and 'Right' sections with 'All Fibres' dropdowns and 'Switch' buttons.
- Crate Configuration:** A panel with 'Sel. FEB' and 'Hdr. FEB' fields and 'Clear', 'Fill', and 'Apply' buttons.
- Power State and Control:** A panel on the right with 'FEB' (Masked/Enabled), 'Power State' (ON/OFF), and 'Power Control' (OFF/ON) indicators.
- Visual Representation:** A central grid of 16 vertical bars representing the crate slots, with colored dots indicating various status levels.

At the bottom of the interface, there is a footer with the text: 'LHCb calorimeter meeting (jean-luc PANAZOL)' and a page number '7'.

# Example : Electronique board TVB

The screenshot displays the Vision\_1: TVBDU software interface, which is used for monitoring and configuring the TVB (TVBDU) board. The interface is divided into several sections:

- Card Infos:** Displays TVB (-99), Slot (-99), Crate (EC16), Rack (L3B11M), and Master ID (14).
- DP Connection:** Shows Script running (green indicator) and RDB connection (green indicator). Includes buttons for Set RDB parameters and DPE history.
- Archiving Infos:** Includes a Set RDB parameters button and a DPE history button.
- Project Infos:** Shows PC (ecdaqhvc01w) and Project (ECDAQC1). Includes buttons for Stop Monitoring, Start/Restart Monitoring, Last loaded recipe (PHYSICS/Configure), Recipe files infos, and Loaded recipes.
- Firmware version:** Shows HCAL and EPPI versions.
- Registers Comparison with Recipe:** Shows FPGA (HCAL, EPPI) and Temperature (under Mezzanine, between FPGA) indicators (red).
- GOL:** Shows HCAL indicator (red).
- Others:** Shows Delay Chip indicator (red).
- Front Clocks Comparison with Recipe:** Displays a grid of clocks (PSSPD0-7, ECAL0-7) for EPPI and HCAL. The caption is Correct (green).
- Delays Comparison with Recipe:** Displays a grid of delays (PSSPD0-7, ECAL0-7, HCAL0-3) for EPPI and HCAL. The caption is Correct (green).

At the top, there is a progress bar with the text "Please wait while summarizing..." and a Help button. The interface also includes a menu bar (Module, Panel, Scale, Help) and a toolbar with various icons.

08/09/2009

LHCb calorimeter meeting

ECDAQC1:EC16TVB2: Error in checking temperature sensor between FPGA with PHYSICS/Configure: -1|-1|-1|-1  
ECDAQC1:EC16TVB2: Error in checking HCAL GOL with PHYSICS/Configure: -1|-1|-1|-1  
EPDAQC1:EC16TVB2: Error in checking EPPI GOL with PHYSICS/Configure: -1|-1|-1|-1



# Exemple : Electronique Board FEB

The screenshot displays the 'Vision\_1: FEBXCAL\_Main' software interface. The main window title is 'Vision\_1: FEBXCAL\_Main'. The interface is divided into several sections for configuring the FEBXCAL board.

**Top Section:** Includes tabs for 'FEB', 'Channel FEB', and 'LO Trigger FEB'. The 'FEBXCAL' field contains 'ECDAQC1:EC16FEB1'. There are buttons for 'Apply', 'DEFAULT', 'Save', and 'Update'. A 'Recipe' dropdown menu is also present.

**Derandomizer state:** Contains buttons for 'Front-End' and 'Sequencer'.

**LED:** A green button labeled 'OFF'.

**Reset:** Contains buttons for 'Glue', 'Seq PGA', a dropdown menu set to 'ALL', and 'FE PGA'.

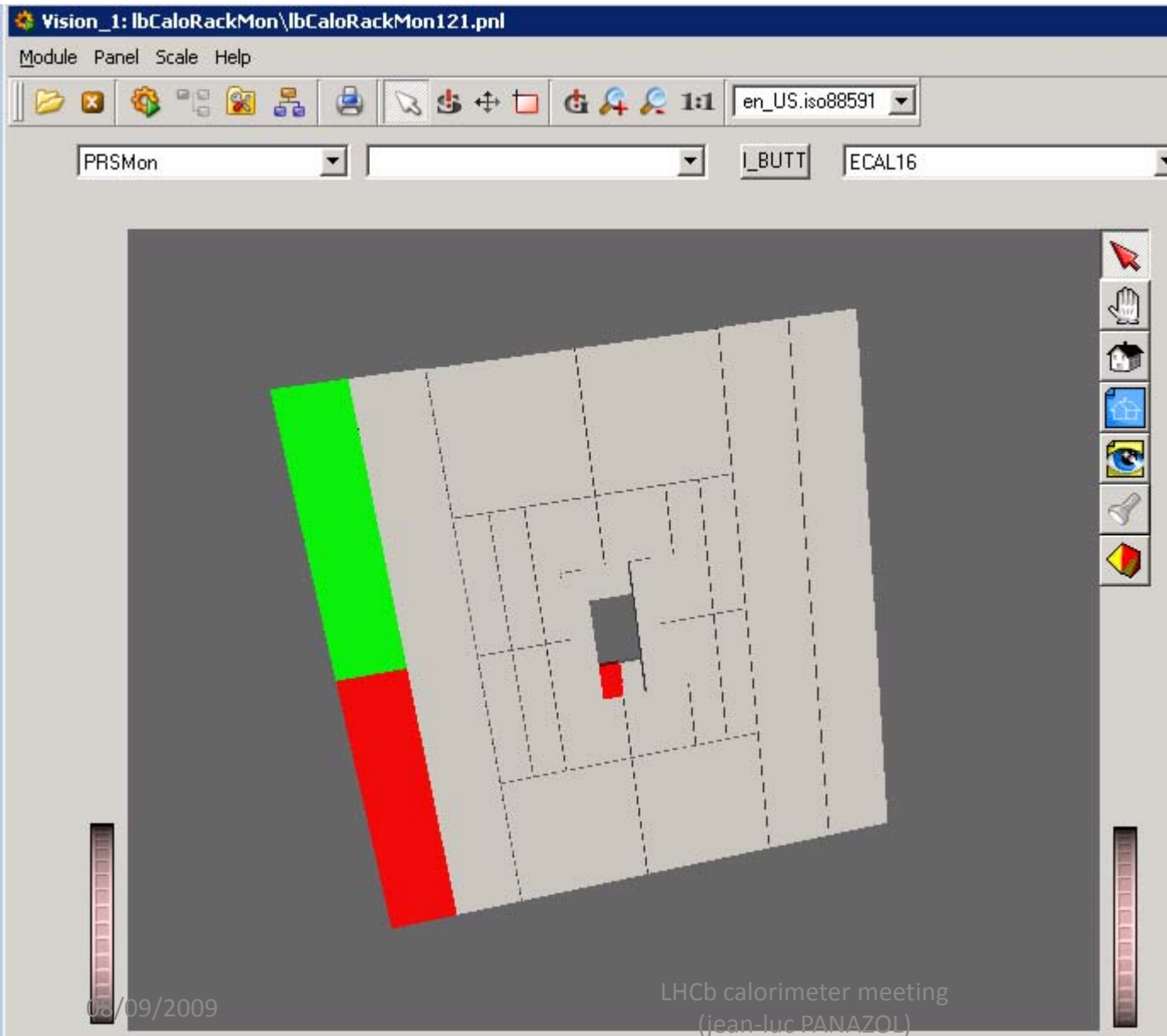
**FEB Identification:** Includes input fields for 'Crate' (0), 'Slot' (0), and 'Number' (0). A text field contains 'PHYSICS'.

**Sequencer Configuration:** Includes checkboxes for 'Clock Inv', 'Calib Mask', and 'Spy Mode'. It also has input fields for 'Global Sync' (0), 'BX ID Reset' (0), and 'FIFO Derando.' (0). There are also input fields for 'Spy Length' (0), 'PRS Latency' (0), and 'Calib Latency' (0). An 'Apply' button with a green indicator light is at the bottom right of this section.

**Mode:** A text input field.

**Front-End Configuration:** Includes a 'Latencies' section with input fields for 'LO Latency [0...15]' (0), 'LO Latency [16...31]' (0), and 'Test Length' (0). A 'Subtraction' section has radio buttons for 'No Subtraction', 'Subtraction : min(evt-1,evt-2)', and 'Subtraction : min(evt-1,var)'. There is also an input field for a value (0). A 'RAM' section has radio buttons for 'Clock', 'L0', and 'ChannelB', and a checkbox for 'RAM Loop Enabled'. An 'Apply' button with a green indicator light is at the bottom of this section.

# Exemple : ECAL monitoring



# Exemple : Main view

